

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshiharu Kirakata et al.                      Art Unit : Unknown  
Serial No. : New Divisional Application                      Examiner : Unknown  
Filed : September 8, 2003  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Prior to examination, please amend the application as indicated on the following pages.

**Amendments to the Specification** begin at page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 12 of this paper.